



**AMENDMENT UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE -  
EXAMINING GROUP 2823**

**PATENT**

Attorney Docket No.: 00939H-087800US

Client Ref. No.: P03H6003/US/yl

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

O.K To

ENTER

In re application of:

Kwan-Yong Lim et al.

Application No.: 10/750,084

Filed: December 30, 2003

For: METHOD FOR FABRICATING  
TRANSISTOR WITH POLYMETAL  
GATE ELECTRODE

Customer No.: 20350

Confirmation No. 1874

Examiner: Trung Q. Dang

Technology Center/Art Unit: 2823

**AMENDMENT UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE EXAMINING  
GROUP 2823**

T. DANG

11/15/05

**Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

**Date Amend Filed: October 26, 2005**

Sir:

In response to the Final Office Action mailed July 27, 2005 on the above-referenced application, please enter the following amendments and remarks:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.